	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Error
1	BRS	L2	20	1 and (CMOS or DMOS or LDMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/17 11:56			0
2	BRS	L3	7211	(("257/488") or ("257/491") or ("257/496") or ("257/500") or ("257/302") or ("257/328") or ("257/329") or ("257/330") or ("257/332") or ("257/335") or ("257/339") or ("257/341") or ("257/409"))	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/17 11:55			0
3	BRS	L4	2161	3 and (CMOS or DMOS or LDMOS or LDDMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/17 11:55			0
4	BRS	L5	366	4 and (angle or slant$4 or oblique)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/17 11:55			0
5	BRS	L6	366	5 and (body or region or gate or adjac$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/17 11:56			0

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